

APPLICATION DATA SHEET

Electronic Version 0.0.11

Stylesheet Version: 1.0

Attorney Docket Number: **740756-2440**

Publication Filing Type:

new-utility

Application Type:

utility

Title of Invention:

METHOD FOR FABRICATING THIN-FILM TRANSISTOR**Legal Representative:**

Attorney or Agent: Eric J. Robinson

Registration Number: 38285

Attorney or Agent: Jeffrey L. Costellia

Registration Number: 35483

Attorney or Agent: Donald R. Studebaker

Registration Number: 32815

Attorney or Agent: Jason H. Vick

Registration Number: 45285

Attorney or Agent: Charles M. Leedom

Registration Number: 26477

Attorney or Agent: David S. Safran

Registration Number: 27997

Customer Number Correspondence Address: 22204***22204*****Continuity Data:**

This application is a division of 08/089,650 A1 1993-07-12 PENDING

which is a continuation of 07/707,092 A1 1991-05-29 US Abandoned

Foreign Priority:

02-145069

JP

1990-06-01

Priority Claimed

Assignee (Publish): Semiconductor Energy Laboratory Co., Ltd.

398, Hase,

Atsugi-shi 243-0036 Kanagawa-ken JP

INVENTOR(s):

Primary Citizenship: JP

Given Name: Hongyong

Family Name: ZHANG

Residence City: Yamato-shi

Residence Country: JP

Address: Paresu Miyagami 302, 1-10-15, Fukamidai,

Yamato-shi Kanagawa, 242 JP